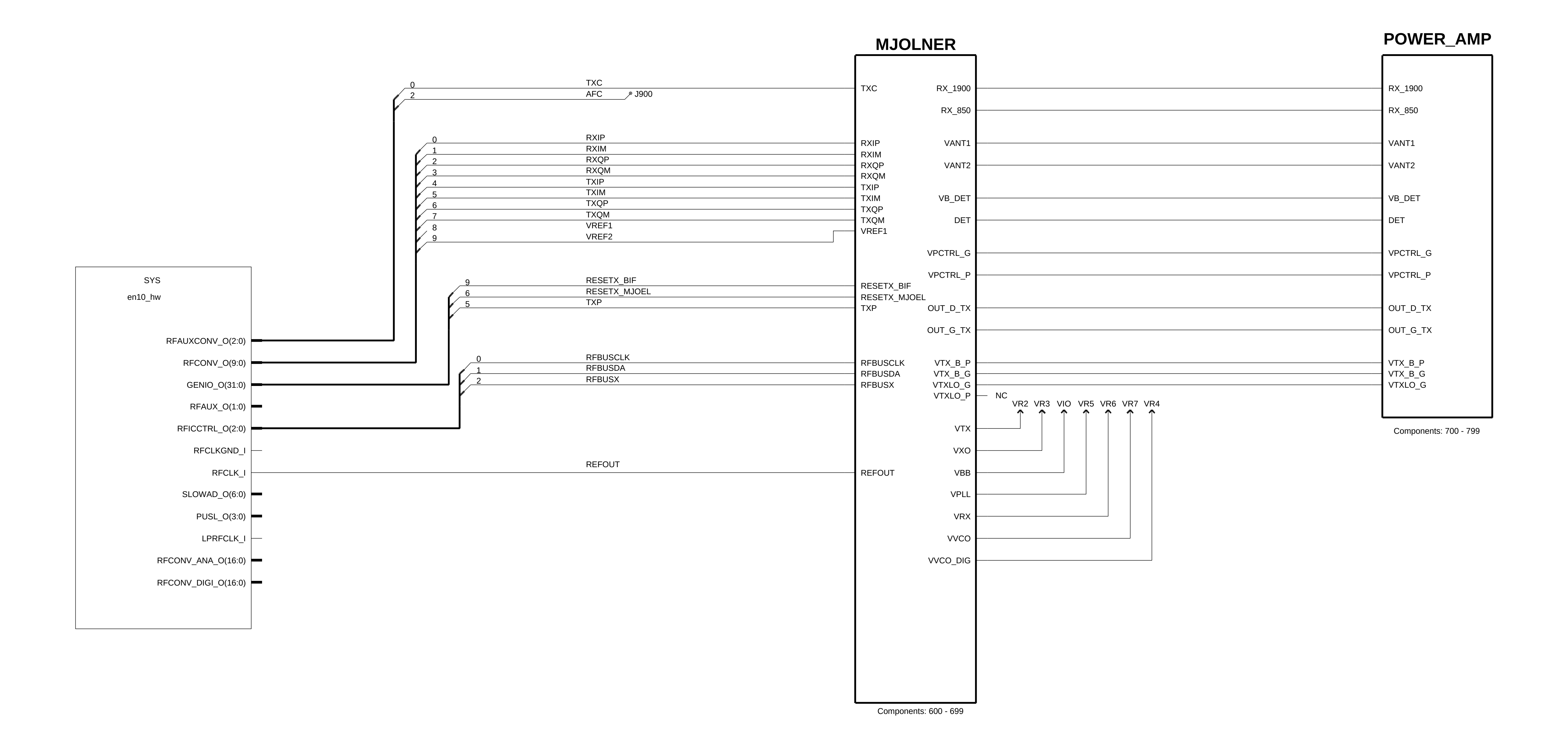
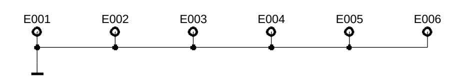
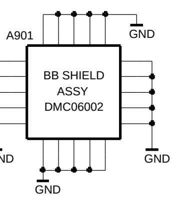
Top Level Schematic (FDK VCO)

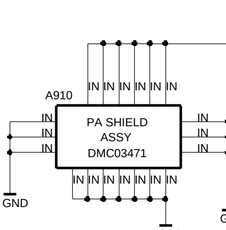


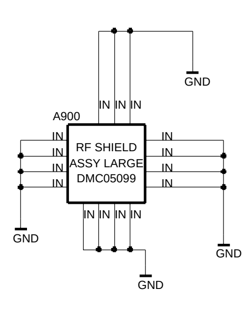
Shield Sheet for FDK VCO



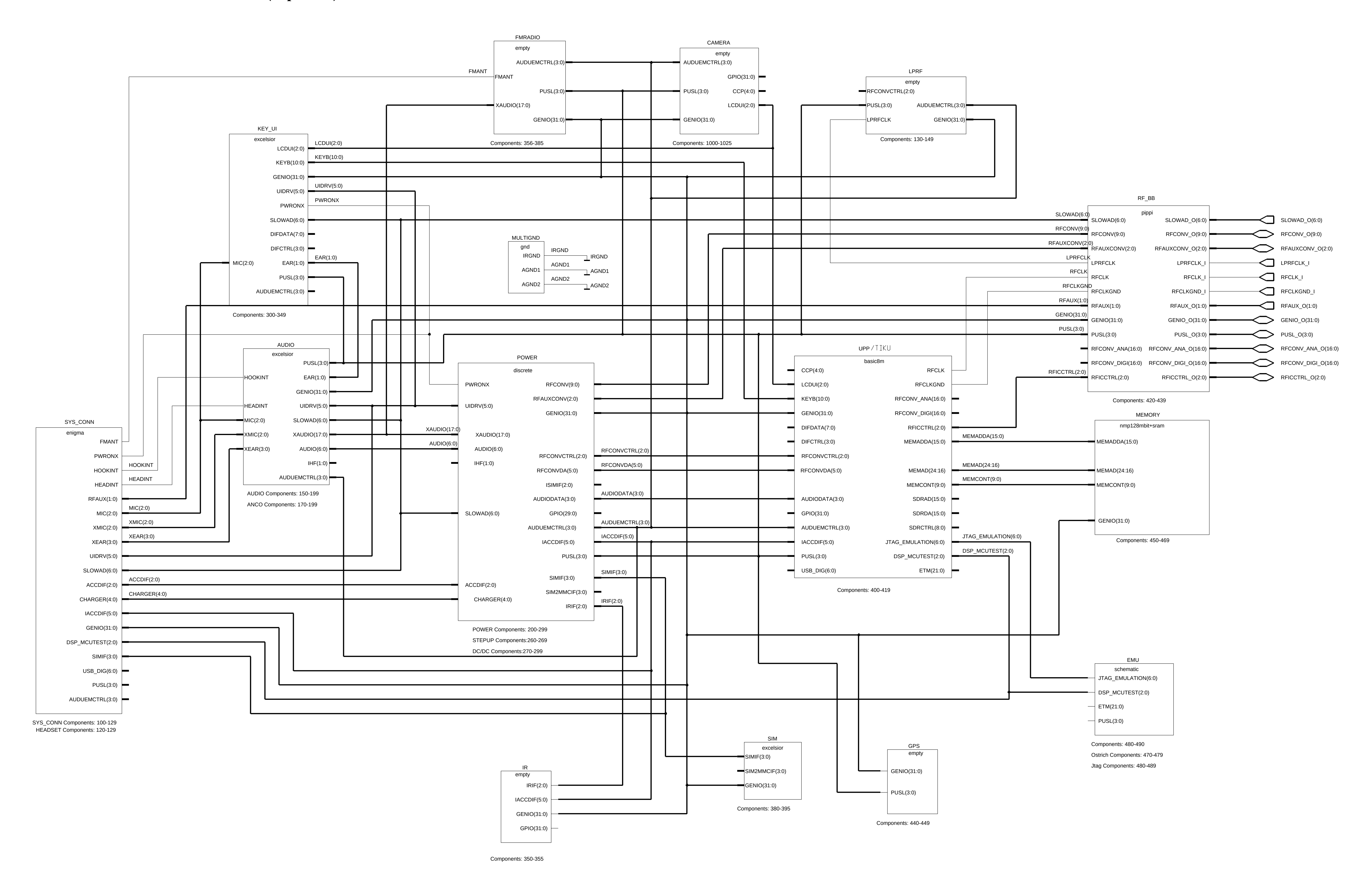
E008
GND



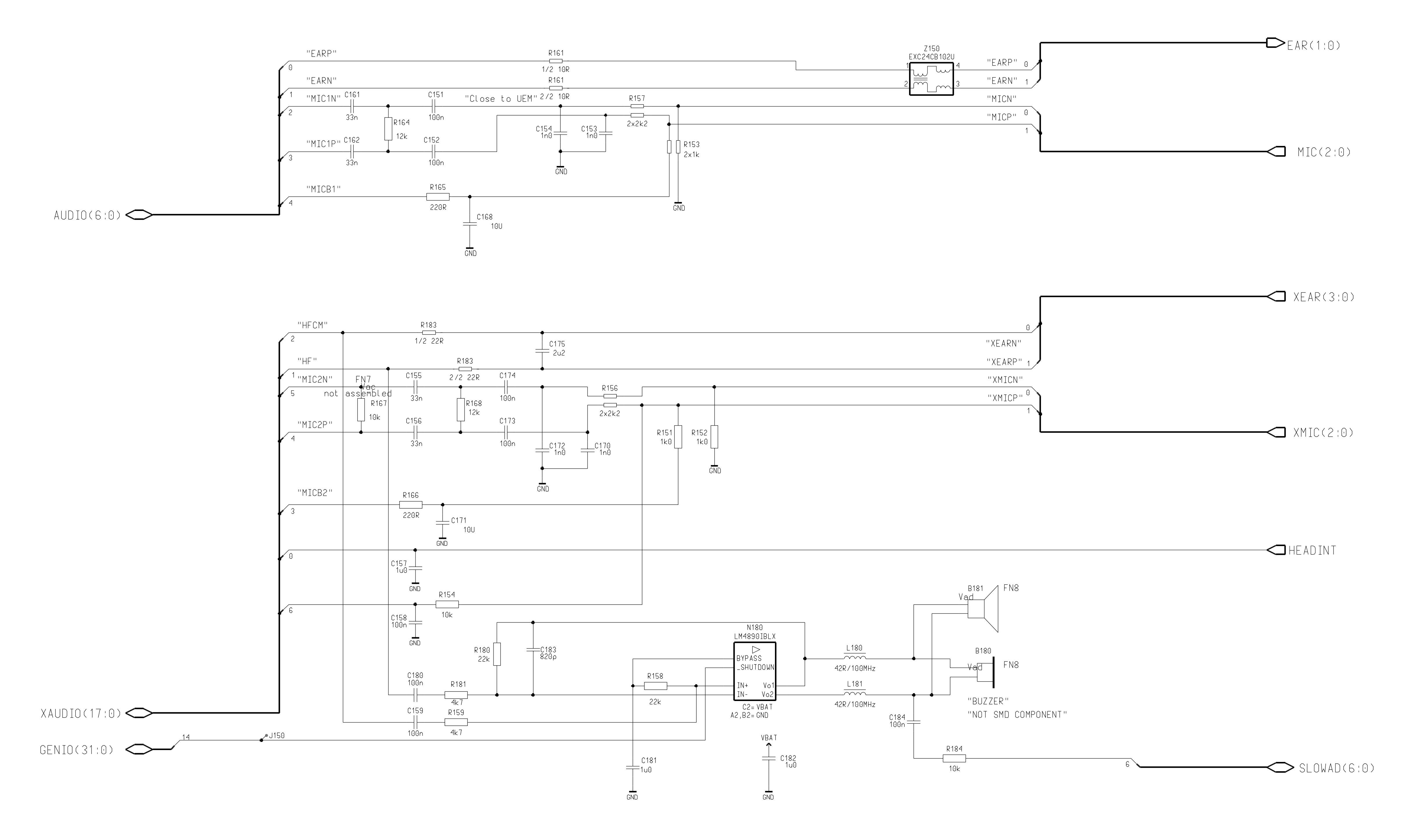




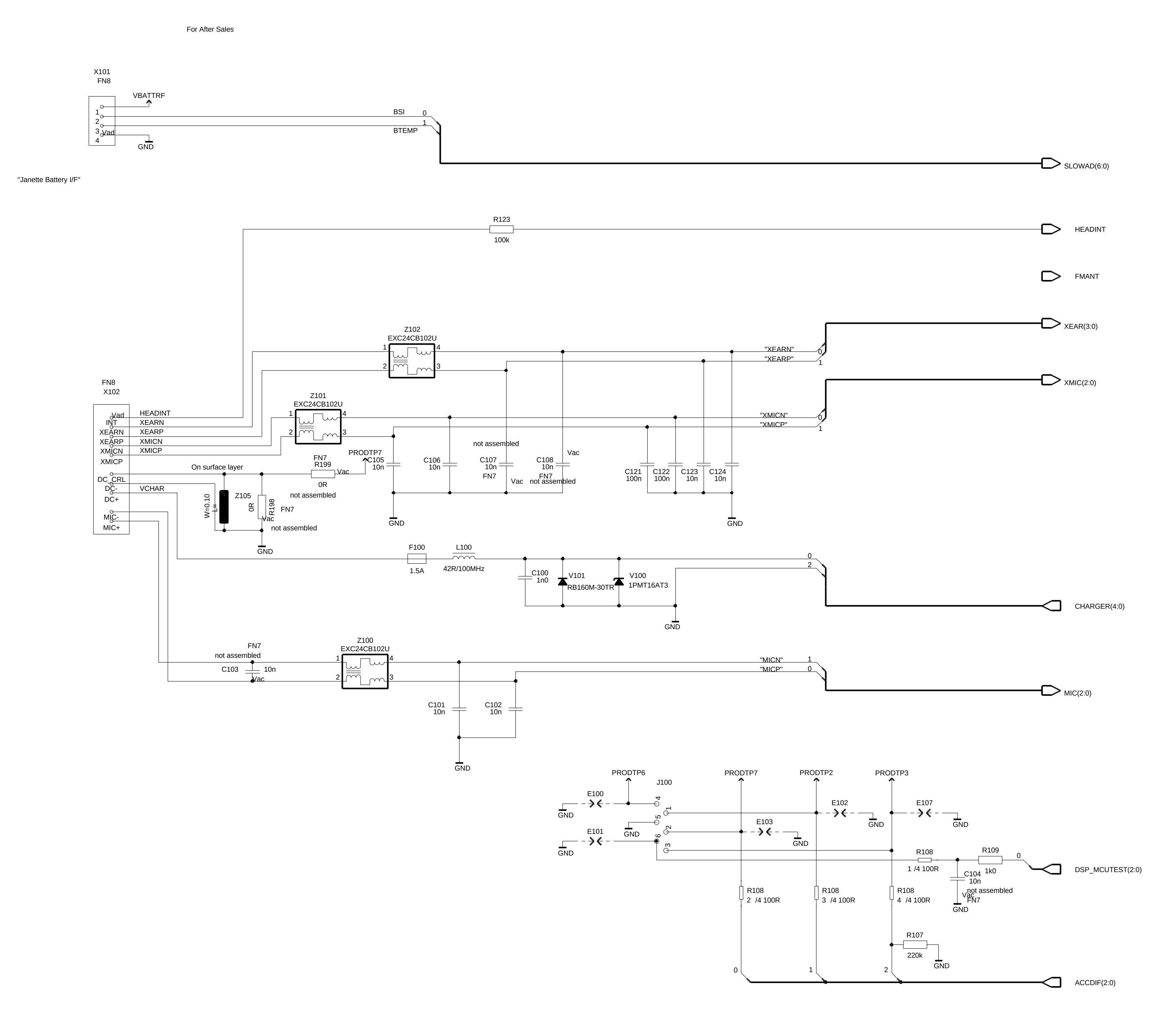
DCT4 Common Baseband Schematic (Top Level)



Audio Interface

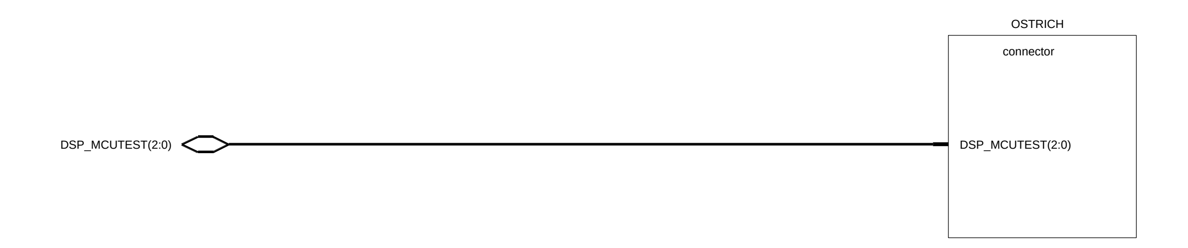


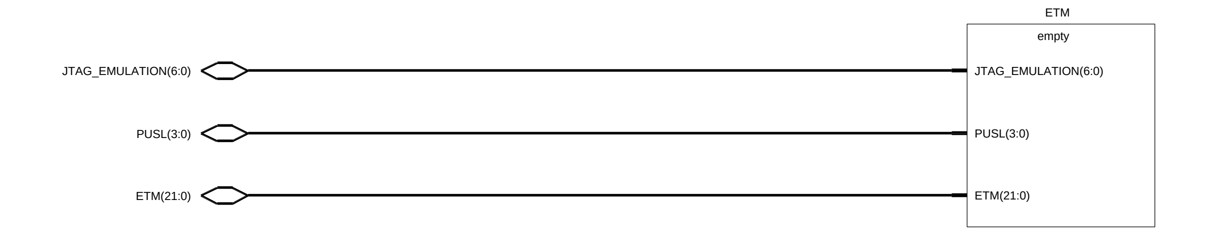
System Connector



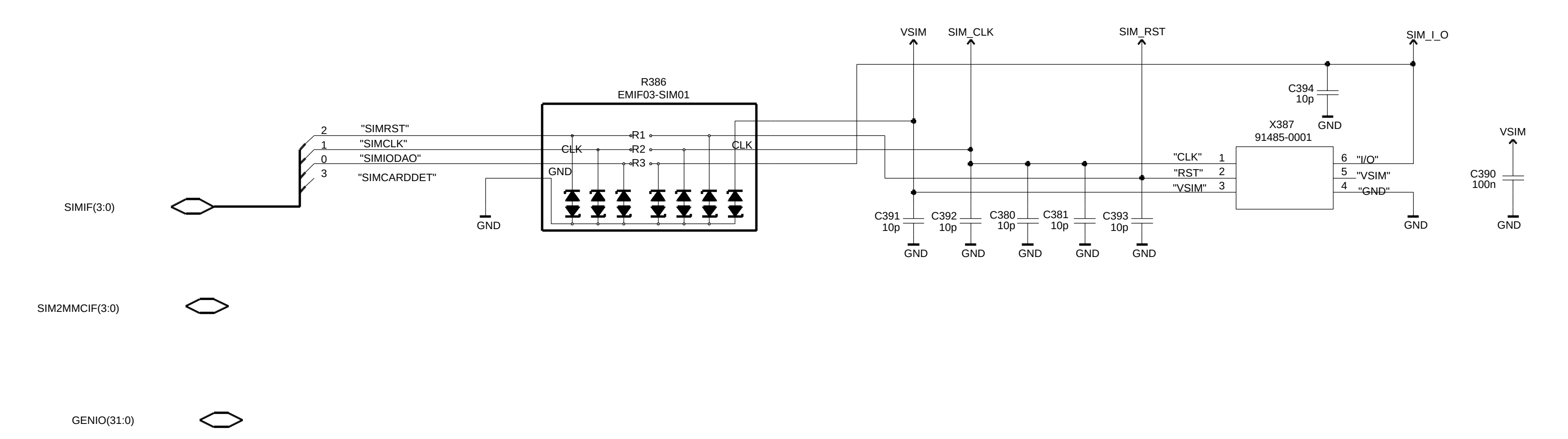
Test and Emulator Interface



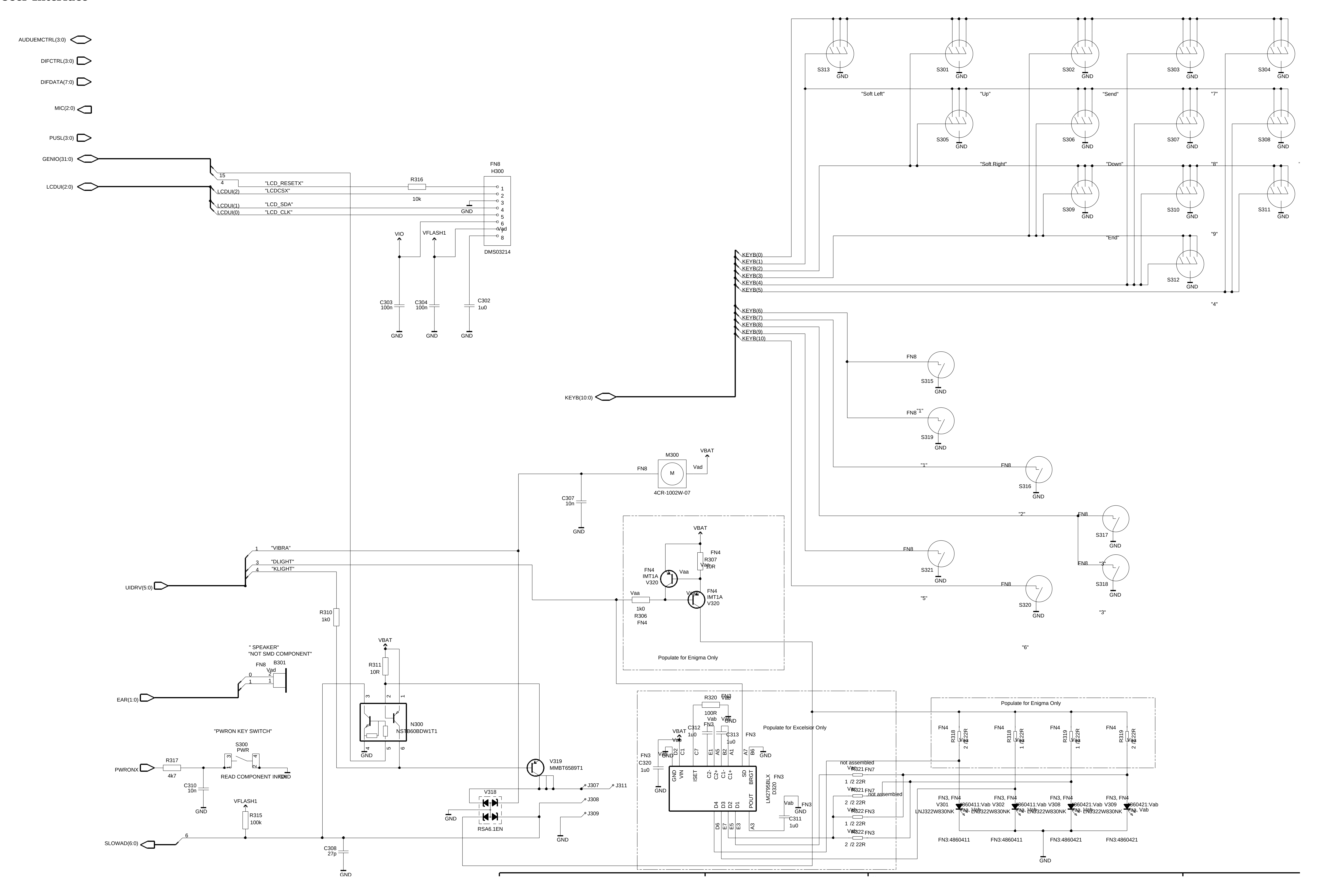




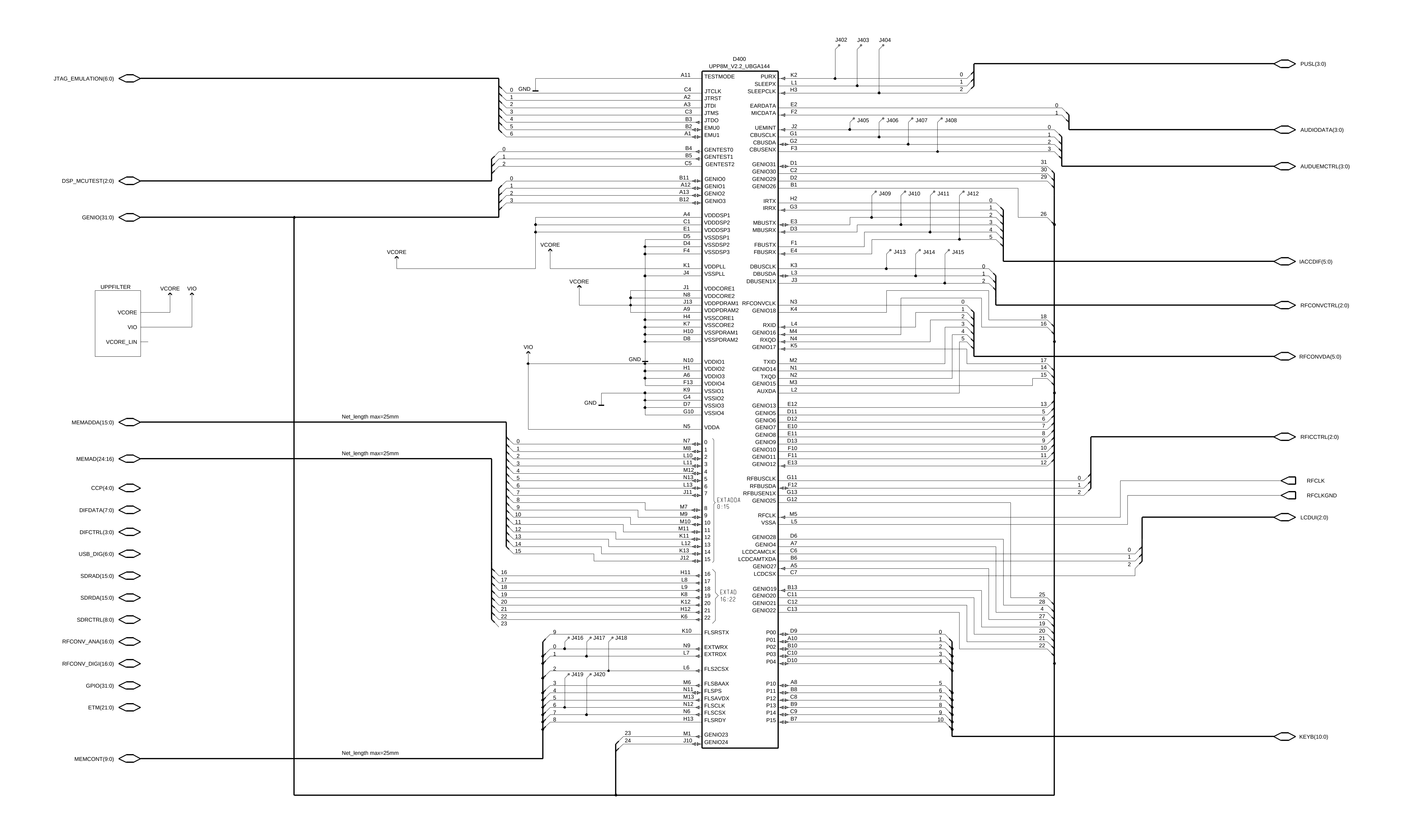
SIM Reader



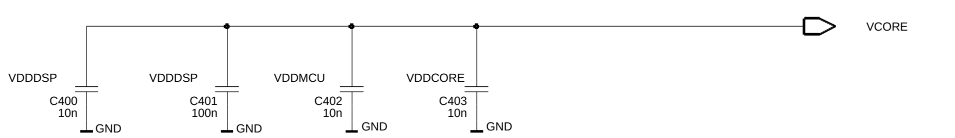
User Interface



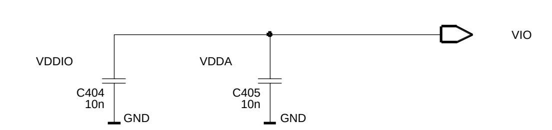
UPP 8M Implementation



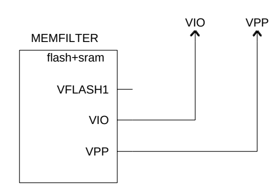
Discrete decoupling capacitors for UPP



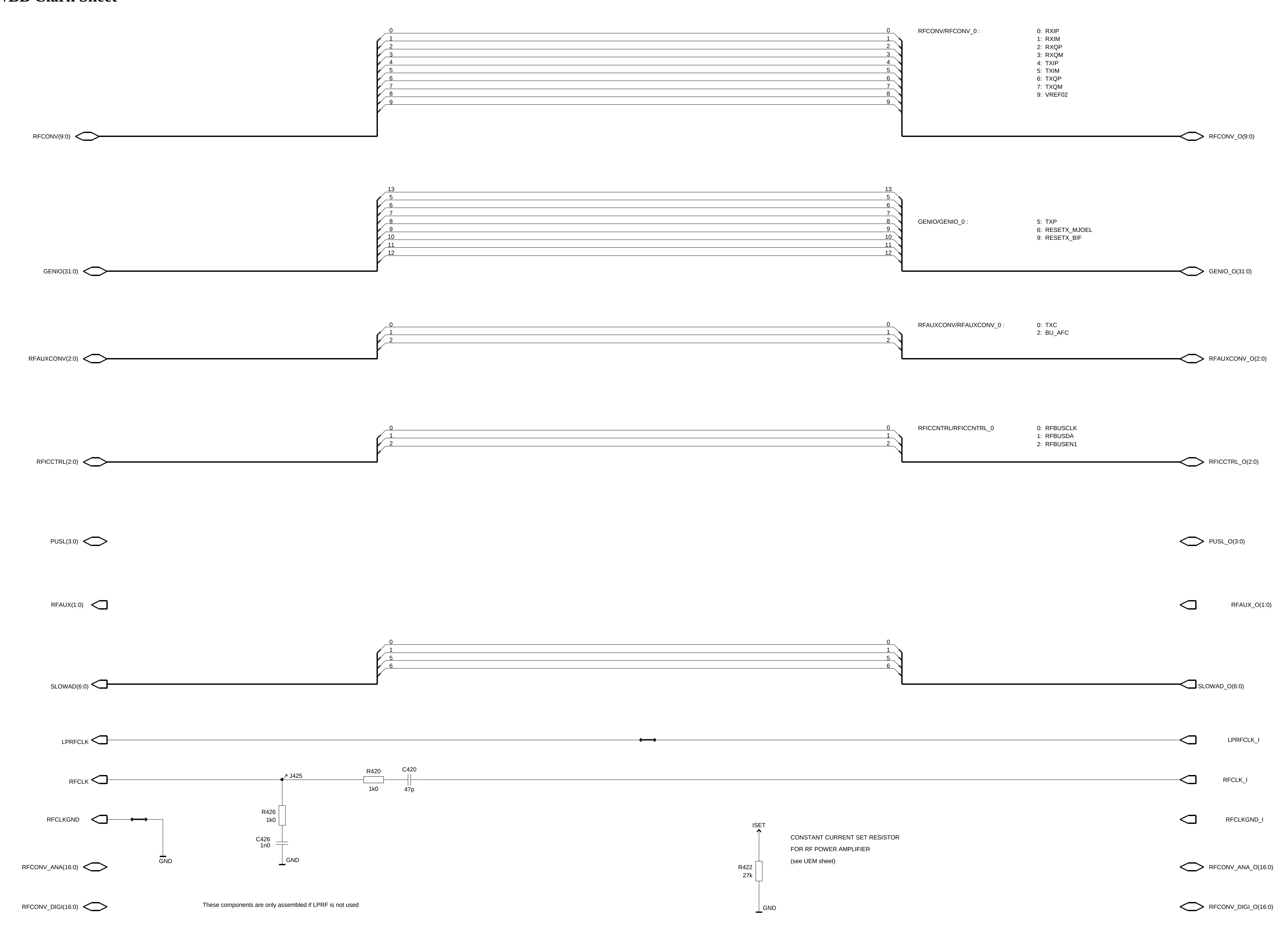




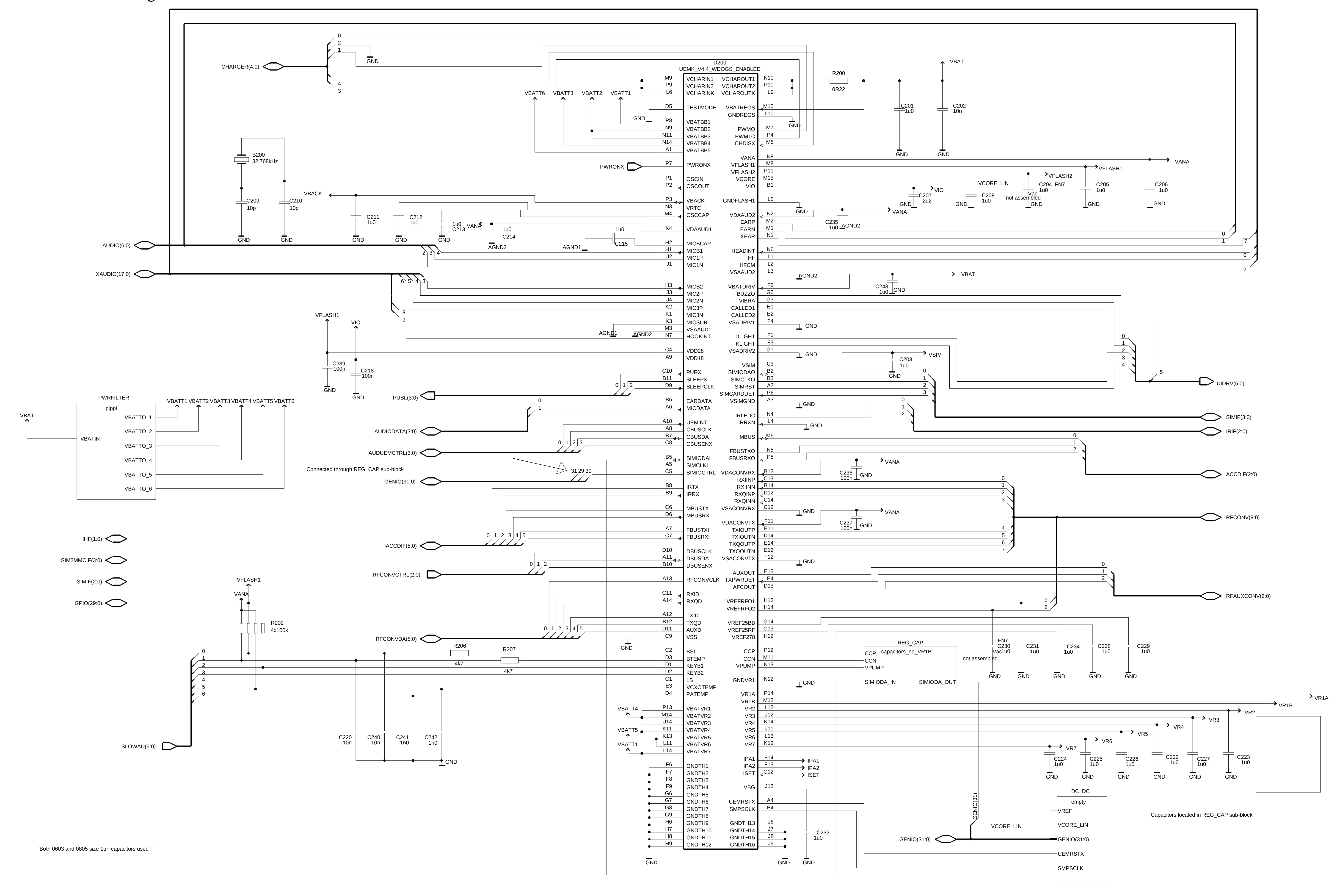
Intel 128 Mbit + 4 Mbit SRAM



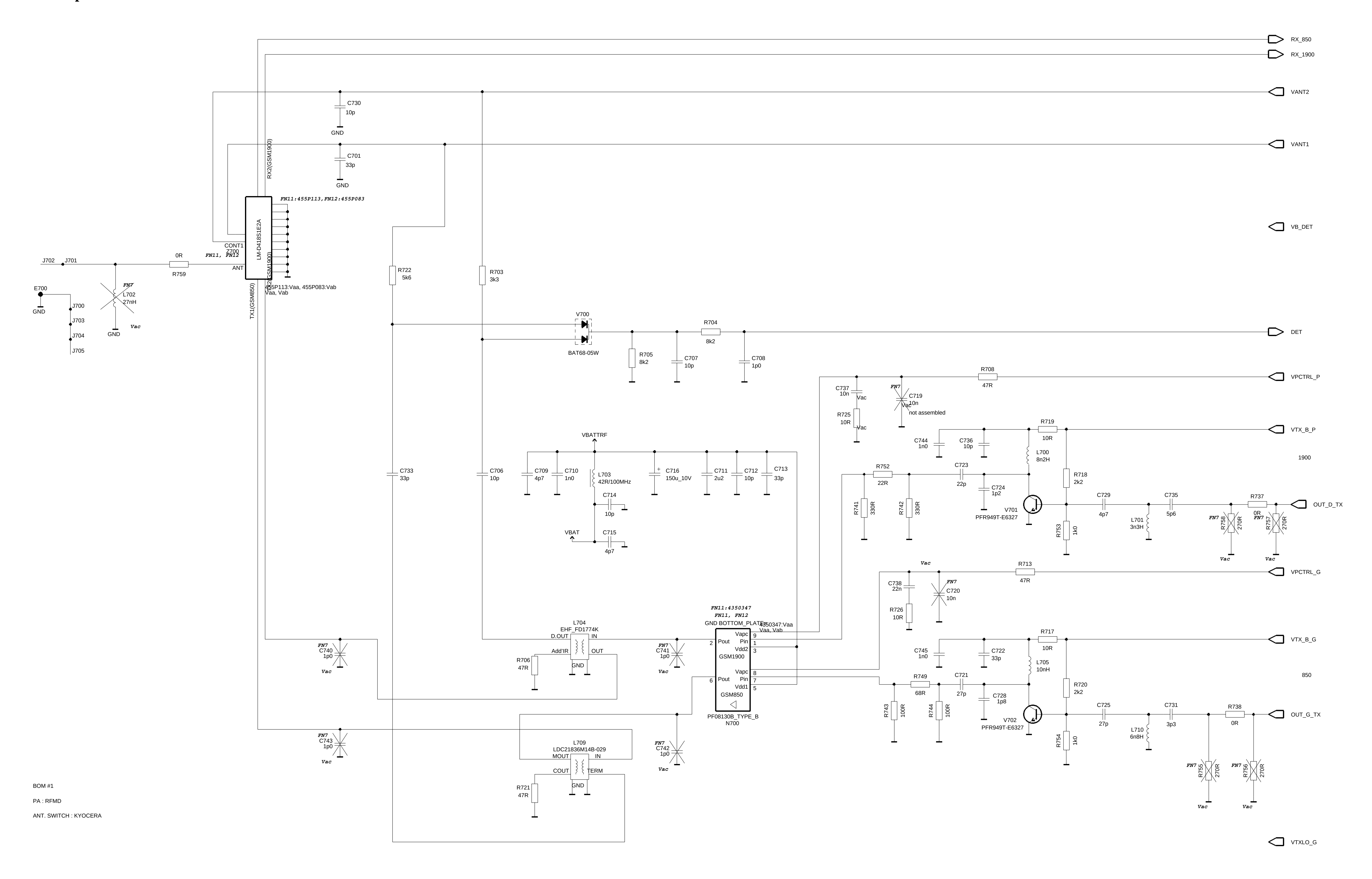
RF/BB Clark Sheet



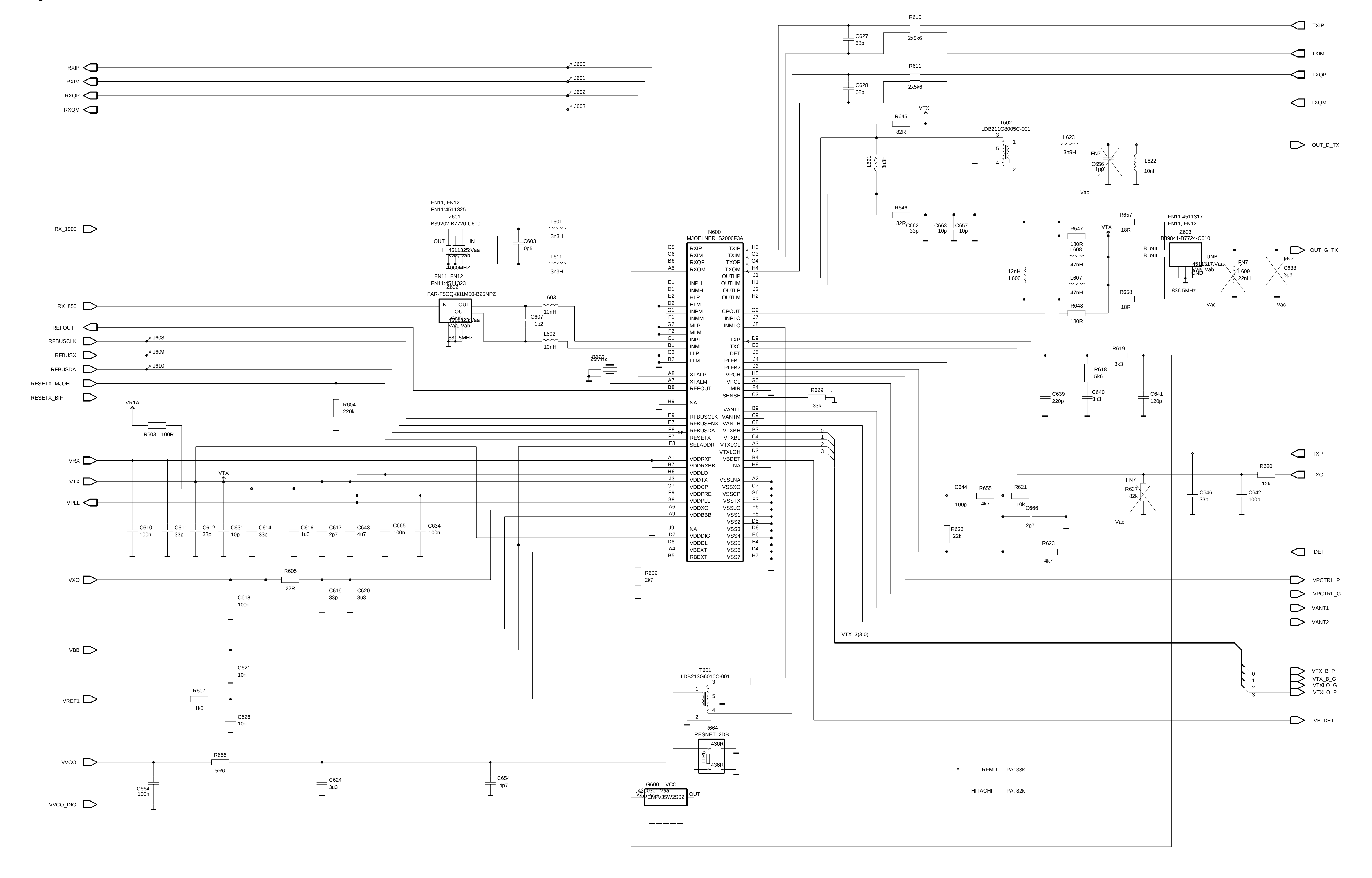
Discrete Power Management



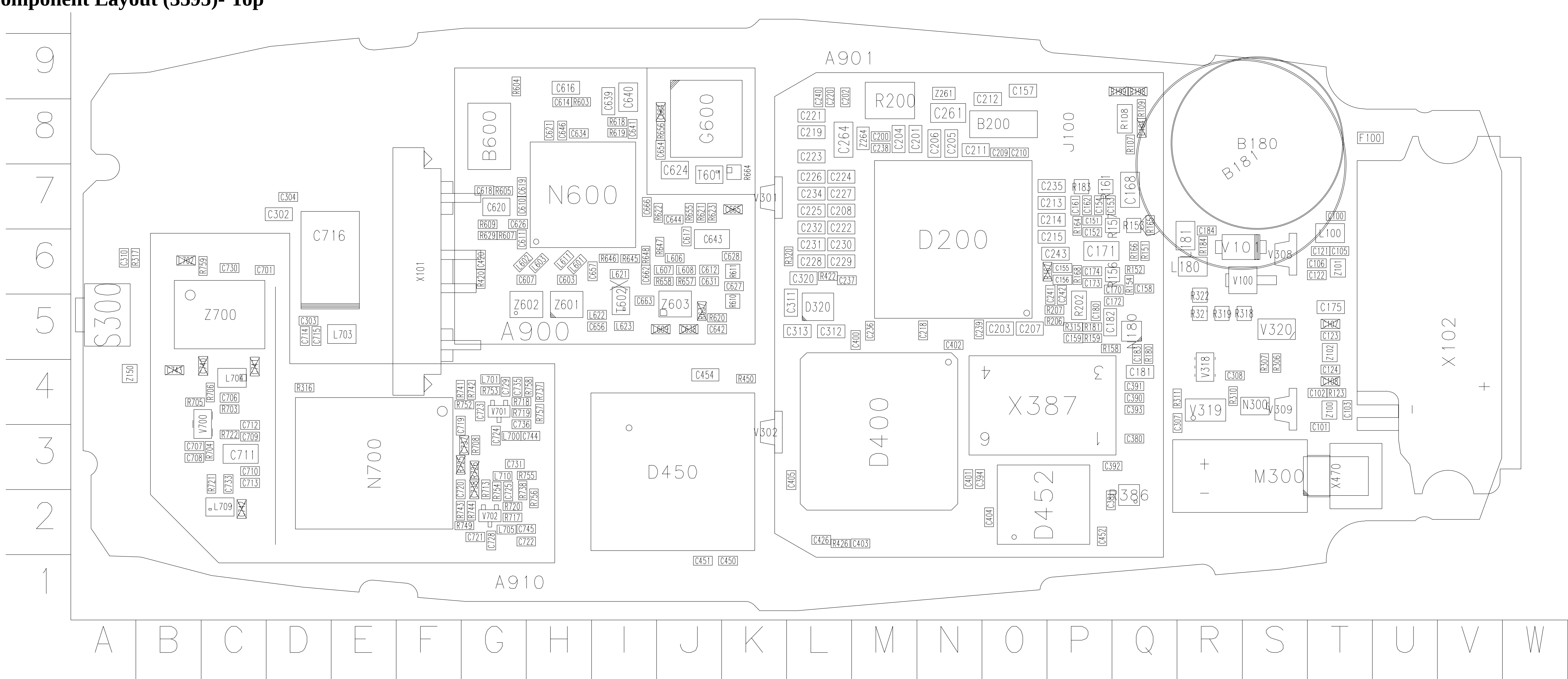
Power Amplifier



Mjoelner



Component Layout (3595)- Top



Component Layout (3595) - Bottom

